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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10074840	FILING DATE 02/13/2002	CLASS 438	SUBCLASS C	GAU 2827	EXAMINER 2827
**APPLICANTS: Farnworth Warren;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/640,801 08/17/2000, <i>now US PAT 6432752</i>					
<i>OK</i>					
NOTE-DISCLAIMER The term of this patent shall not extend beyond the expiration date of Pat. No. <i>6432752</i>					
** FOREIGN APPLICATIONS VERIFIED: <i>None</i>					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO 4212.3 (99-0306.3)	
Verified and Acknowledged Examiners's initials <i>OK</i>					
TITLE : Stereolithographic methods for fabricating hermetic semiconductor device packages and semiconductor devices including stereolithographically fabricated hermetic packages					
U.S. DEPT. OF COMM. / PAT & TM-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED <i>10/1/02</i>		David A. Zarnke Assistant Examiner		CLAIMS ALLOWED Total Claims <i>26</i> Print Claim for O.G. <i>1</i>	
ISSUE FEE Amount Due <i>\$1580</i> Date Paid		ALBERT W. PALADINI PRIMARY EXAMINER <i>Albert W. Paladini 9-70-02</i> Primary Examiner PREPARED FOR ISSUE		DRAWING Sheets Drwg. <i>9</i> Figs. Drwg. <i>23</i> Print Fig. <i>1</i>	
<input checked="" type="checkbox"/> TERMINAL DISCLAIMER		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.		Application Examiner <i>10/2/02</i>	

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